

Abstract of the Disclosure

Systems and methods are provided using dynamically adjustable differential output drivers. Integrated circuits such as programmable logic devices
5 may be provided with adjustable differential output drivers for transmitting high-speed data to other integrated circuits. The peak-to-peak output voltage and common-mode voltage of the output drivers may be adjusted. Dynamic control circuitry may be used to
10 control the settings of current sources, programmable resistors, and voltage source circuitry in the adjustable differential output driver automatically in real time. The adjustable components in the differential output driver may be adjusted by the
15 dynamic control circuitry based on feedback information received from the integrated circuit to which the data is transmitted.